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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): John S. Yates, Jr., et al.

Serial No.: 09/239,194

Art Unit: 2127

Filed: January 28, 1999

Examiner: Kenneth Tang

Title: EXECUTING PROGRAMS OF A FIRST COMPUTER ARCHITECTURE ON  
A COMPUTER OF A SECOND ARCHITECTURE

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant wishes to make of record the items listed on the accompanying Form PTO-1449. Applicant respectfully requests the Examiner to fully consider the items and independently ascertain their teaching before issuance of the next action, and to make them of record in the file. The Examiner is also requested to initial and return a copy of the enclosed Form PTO-1449 to evidence such consideration.

1. Applicant has listed publication dates on the attached Form PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated. Applicant reserves the right to establish the patentability of the claims over any information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered. This Information Disclosure Statement should not be construed as a representation that information more material to the examination of this application does not exist.

2. The references listed on the enclosed Form 1449 are references that have come to light in applications listed in the Information Disclosure Statement of October 2000. For many of the listed references, pertinence is mentioned in the respective application file in which the reference came to light. Applicant has not reviewed all of them in detail. Of those that have been reviewed, none of the references are believed to be any more pertinent than the references provided in earlier IDS' and Forms 1449. However, in an abundance of caution, Applicant requests that they be considered.

3. The two Intel references have been cited before, and are now cited with improved and corrected bibliographic information.

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on January 27, 2005 as First Class Mail in an envelope with sufficient postage addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.




4. This Information Disclosure Statement is being filed at a time when a nominally-final Office Action has been mailed, but when finality of that Office Action should be withdrawn, or when prosecution may be reopened on withdrawal or reversal of all pending rejections. In the event of reopening of prosecution, entry of this Information Disclosure Statement is proper on payment of a fee pursuant to 37 C.F.R. §1.97(c)(2). Upon such entry, charge the fee due under C.F.R. §1.17(i)(1) to Deposit Account No. 23-2405, Order No. 114596-03-4000.

5. The Commissioner is hereby authorized to charge any additional fees that may be required for this Information Disclosure Statement, or credit any overpayment, to 23-2405, Order No. 114596-05-4013.

Respectfully submitted,

WILLKIE FARR & GALLAGHER, LLP

Dated: January 27, 2005

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FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

114596-05-4013

SERIAL NO.

09/239,194

APPLICANT

John S. Yates, Jr., et al.

FILING DATE

January 28, 1999

GROUP ART UNIT

2127

**INFORMATION DISCLOSURE CITATION**

(Use several sheets if necessary)

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,355,487	11/11/1994	Keller	717	127	Jul. 23, 1993
	5,404,473	04/04/1995	Papworth	712	241	Mar. 1, 1994
	5,721,855	02/24/1998	Hinton	712	218	Jul. 12, 1996
	5,729,728	03/17/1998	Colwell	712	234	Sep. 6, 1996
	6,157,993	12/5/2000	Lewchuk	711	213	Jan. 3, 2000
	6,256,775	7/3/2001	Flynn	717	127	Dec. 11, 1997
	6,351,646	02/26/2002	Jellema	455	461	Apr. 20, 1998
	6,535,903	3/18/2003	Yates	718	100	Jan. 29, 1996
	6,553,431	04/22/2003	Yamamoto	710	8	Jul. 21, 1999
	6,708,173	03/16/2004	Behr	707	10	Oct. 18, 2000
	6,721,941	4/13/2004	Morshed	717	127	Aug. 22, 2000

**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Papers, Etc.)

		Intel Corporation, Intel Architecture Software Developer's Manual, Volume 1: Basic Architecture, pages 3-10 to 3-13, 7-1 to 7-3, 7-20 to 7-25; Volume 2: Instruction Set Reference, pages 3-60 to 3-63, 3-240 to 3-251; Volume 3: System Programming Guide, pages 11-1 to 11-3, 11-10 to 11-13, 14-1 to 14-3, 14-10 to 14-15 (1997)
		Intel Corporation, Pentium Processor Family Developer's Manual (1997), pages 1-1 to 1-6, 2-1 to 2-20
		Intel Corporation, Pentium Processor Family Developer's Manual (1997), vol. 3: Architecture and Programming Manual, pages 3-1 to 3-3, 3-10 to 3-13, 12-1 to 12-27, 14-1 to 14-30
		Peterson, "The Profile Naming Service," ACM Transactions on Computer Systems (TOCS), v.6 n.4, p.341-364 (Nov. 1988)

**EXAMINER****DATE CONSIDERED**